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Applicant(s): Devins et al.			BUR920010098US1
Application No. 09/683,677	Filing Date 2/1/2002	Examiner Saif A. Alhija	Group Art Unit 2128
Invention: METHOD OF SWITCHING EXTERNAL MODELS IN AN AUTOMATED SYSTEM-ON-CHIP INTEGRATED CIRCUIT DESIGN VERIFICATION SYSTEM			
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 09/683,677

Applicant(s): Devins et al.

Filed.: February 1, 2002

Art Unit: 2128

Dkt. No.: BUR920010098US1

Examiner: Saif A. Alhija

Title: METHOD OF SWITCHING EXTERNAL MODELS IN AN AUTOMATED SYSTEM-

ON-CILIP INTEGRATED CIRCUIT DESIGN VERIFICATION SYSTEM

Honorable Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

## REQUEST FOR RECONSIDERATION

This Request for Reconsideration is being filed in response to the Office Action mailed on October 26, 2005.